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(1) Question 1 is compulsory. (2) Attempt any four from the remaining questions. (3) Assume data wherever required. (4) Figures to the right indicate full marks. 1 (a) Draw the block diagram of 80386DX processor and explain each block in ...

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Attempt any four from  
the remaining  
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required. (4) Figures to the right indicate full marks. 1 (a) Enlist the instruction pairing rules of U and V pipeline in Pentium.

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CE-303-B CE-305-B  
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Microprocessor  
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microcomputers, and

Assembly Language:

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Instruction Set and

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Microprocessor  
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Microcomputer  
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Architecture and its  
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I/O Devices,  
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memory system

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design, pipelining, and other techniques to exploit parallelism. ...

**CS 146: Computer Architecture - Computer Science**

EC8681

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